

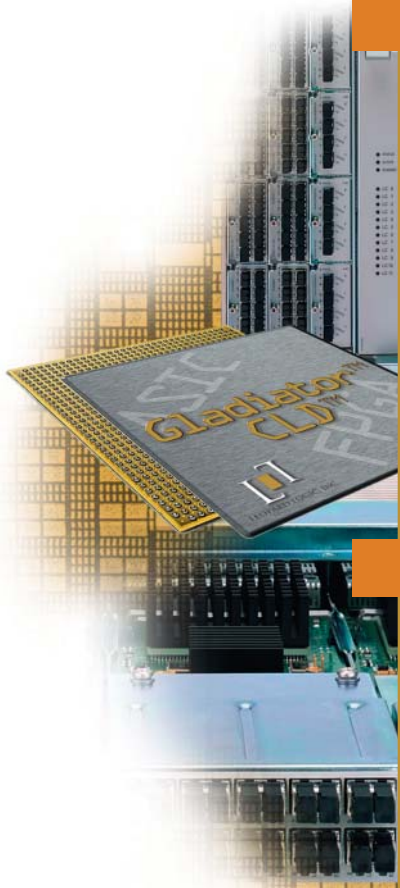


Fact Sheet



LEOPARD LOGIC, INC.

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The Company

Leopard Logic, headquartered in Cupertino, CA, is a venture-backed, fabless semiconductor company offering a new class of digital logic devices. Founded in November 2000 by a seasoned team of configurable logic experts, Leopard Logic has developed products that represent a significant advancement over legacy logic devices.

The Gladiator™ CLD™ family of configurable logic devices is the first solution to combine Application Specific Integrated Circuit (ASIC) and Field Programmable Gate Array (FPGA)

technologies in a single device and design environment. This unique combination enables system designers to increase performance, reduce power consumption and board space, and slash costs, when compared to discrete FPGA and ASIC implementations.

Complemented by leading edge third-party Intellectual Property (IP) cores, tools, and system integration services, Leopard Logic provides a complete solution for the fast and successful implementation of Gladiator CLD configurable logic devices into customer applications.

The Case For Configurable Logic

Boom or bust, some things just don't change. It's still all about time and money, as companies have to decide how to implement their next design and what implementation platform to choose.

Rapidly increasing mask costs and lengthening ASIC design cycles necessitate new solutions to produce custom logic devices on time and on budget. While FPGAs offer an alternative, they come with significant drawbacks such as high unit costs and power consumption, along with limited capacity and performance.

System companies demand flexible, chip-level solutions that allow them to add value and differentiate their products. Shrinking product life cycles and varying

standards require the use of innovative integrated circuit (IC) technologies to meet today's market demands. The only way to achieve high integration efficiency and performance with the needed flexibility on the system level, is to integrate ASIC and FPGA capabilities in the same device.

Leopard Logic's Gladiator CLD device family allows instant design changes like FPGAs, while leveraging the more efficient ASIC logic for fixed blocks of a design. The design flow utilizes existing infrastructure and proven methodologies, while eliminating the need for lengthy back end processing as necessary for ASICs. Gladiator devices enable cost effective solutions that meet performance, density and power objectives.

The Technology

In state-of-the art circuit design the key issue is to solve the interconnect problem, which is the dominating factor driving density, power and performance.

Leopard Logic is addressing these issues with its patent-pending HyperRoute™ and HyperVia™ interconnect technologies. These innovative

interconnect architectures are the key factor in facilitating the exceptional performance, power, density and flexibility found in Leopard Logic's configurable logic devices. The combination of mask-programmable and field-programmable technology culminates in the Gladiator CLD device family.

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Target Markets

Due to their versatile architecture and tool integration, Leopard Logic's devices are ideally suited for a wide range of markets and applications that require flexible logic solutions. The most natural matches are in areas that today use a combination of Application Specific Standard Product (ASSP)/ASIC with standalone FPGAs such as:

- **Networking:** Edge, Access, Aggregation, Framers, Communications Controllers, Backplane Interfaces
- **Storage:** Bridges, Controllers, Interfaces, Glue Logic
- **Wireless:** DSP Acceleration, Chip Rate Processing, Smart Antenna, Bridges, Backplanes, Glue Logic

Across all markets, Gladiator is an ideal fit for the fast and cost-effective implementation of flexible format converters, protocol bridges, bus interfaces, and glue logic functions.

Customer Benefits

Leopard Logic's Gladiator CLD devices address critical requirements faced by today's system designers by offering:

Increased Flexibility

Accommodate evolving technical specifications and market requirements

Fast Turn-Around

Get working silicon in weeks instead of months

Ease-of-Use

Leverage existing infrastructure, IP and proven methodologies

Increased Productivity & Efficiency

Get it right the first time, on-budget and on-schedule

Low Cost-of-Goods-Sold

Manage growing price pressures

Improved Time-To-Revenue

Design and deploy seamlessly with the same device

The Team

Leopard Logic has built an exceptional team of veteran circuit and software designers, complemented by a seasoned executive team. Engineering team members have a wealth of experience from working at leading semiconductor and EDA companies.

Core competencies include the design of FPGAs and related design tools, reconfigurable computing, full-custom circuit design, as well as vertical application expertise in markets such as networking, wireless communications and digital consumer.

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